

Davis Law Group

A Professional Corporation

March 21, 2006

Via facsimile to 571.273.6500

Refund Branch
U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Re: *Deposit Account No. 50-3524 (Refund Request)*
Customer No. 53953

Dear Sir or Madam:

For U.S. Patent Application Serial No. 10/675,432, I faxed the attached *Response to Office Action and Petition for Extension of Time* (requesting a one-month extension of time) to the U.S. Patent and Trademark Office ("USPTO") on Tuesday, February 21, 2006. The *Response to Office Action* was originally due on Thursday, January 19, 2006, so that a one-month extension of time would expire at the end of Tuesday, February 21, 2006, because: (a) February 19, 2006, was a Sunday; and (b) February 20, 2006, was a Federal holiday within the District of Columbia.

On February 23, 2006, my firm's Deposit Account No. 50-3524 was charged \$120 for a one-month extension of time. However, on February 28, 2006, my firm's Deposit Account No. 50-3524 was charged an additional \$330 for a two-month extension of time (i.e., \$120+\$330=\$450).

By this letter, I respectfully ask that you please refund the additional \$330 to my firm's Deposit Account No. 50-3524.

As shown in the attached document: (a) I sent the fax from area code (512), which is located in Austin, Texas, in the Central time zone; (b) the fax was completely received in the USPTO at 12:32 a.m. Eastern time on February 22, 2006, which was 11:32 p.m. Central time on February 21, 2006; and (c) accordingly, in the attached *Response to Office Action and Petition for Extension of Time*, I signed a *Certificate of Transmission*, which certified that the document was being facsimile transmitted to the USPTO on February 21, 2006.

According to MPEP 502.01: "For example, a facsimile transmission to the Office from California starting on a Friday at 8:45 p.m. Pacific time and taking 20 minutes, would be completed at 9:05 p.m. Pacific time. The complete transmission would be received in the Office around 12:05 a.m. Eastern time on Saturday. The receipt date accorded to the correspondence is the date of the following business day, which in this case, would be Monday (assuming that Monday was not a Federal holiday within the District of Columbia). Note however, that if the *Certificate of Transmission* is available (for documents not proscribed by 37 CFR 1.8(a)(2)), then the above facsimile may be considered timely filed on Friday if it contains a

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Certificate of Transmission and is in compliance with 37 CFR 1.8(a)(1)(i)(B) and (ii)"
(emphasis added).

Therefore, in this case:

(a) the *Response to Office Action and Petition for Extension of Time* was timely filed on **February 21, 2006, before expiration of a one-month extension of time**, in compliance with 37 CFR 1.8(a)(1)(i)(B) and (ii);

(b) my firm's Deposit Account No. 50-3524 should not have been charged an additional \$330 for a two-month extension of time; and

(c) I respectfully ask that you please refund the additional \$330 to my firm's Deposit Account No. 50-3524.

If I need to provide additional information or do anything else concerning this request, please let me know. I would appreciate your return correspondence to confirm receipt of this request. Thanks for your help.

Respectfully submitted,

DAVIS LAW GROUP, P.C.

By: 

Michael A. Davis, Jr.
Registration No. 35,488

Attachments

APR-24-2006(MON) 11:04

Davis Law Group, P.C.

(FAX) 512 306 8374

P. 004/017

USPTO 2/22/2006 12:37 AM PAGE 1/001 Fax Server
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 FEB 21 2006



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Date Received:

2/22/2006 12:32:25 AM [Eastern Standard Time]

Total Pages:

17 (Including cover page)

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Received
 Cover
 Page

FEB-22-2006 (MON) 08:38 Davis Law Group, P.C. (FAX) 512 306 8374 P. 004/017

Fax Transmittal

DATE: February 21, 2006 Total Pages (including this cover page): 17

To: Commissioner for Patents
 Examiner: Ryan A. Dore
 Fax No.: 571-273-8300
 Serial No.: 10276432

From: Michael A. Davis, Jr. Davis Law Group, P.C.
 Direct Phone No.: (512) 306-8334 9020 N. Capital of Texas Hwy.
 Direct Fax No.: (512) 306-8374 Bldg. 1, Suite 370
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If you have any problem with this transmittal, please call: (512) 306-8334

Serial No.: 10276432
 Filed: September 22, 2005
 Title: METHOD AND SYSTEM FOR REDUCING POWER CONSUMPTION IN A CACHE
 MEMORY
 Attorney Docket No.: 1001.23

Dear Sir or Madam:

Attached please find the following regarding the above-referenced patent application:

1. Transmittal letter (in duplicate);
2. Pduction for Extension of Time (in duplicate); and
3. Response to Office Action.

Entry of these papers in the above referenced application is conventionally solicited. Any question regarding this matter should be directed to the undersigned.

Michael A. Davis, Jr.
 Michael A. Davis, Jr.
 Registration No. 23,408

Confidentiality Note: The information contained in this facsimile transmittal is privileged and confidential and is intended only for the use of the addressee. The term "privileged and confidential" includes, without limitation, attorney-client privileged communications, attorney work product, trade secrets, and any other proprietary information. Nothing in this transmittal is intended to be disclosed to the public or to any other person. If the sender of this message is not the intended recipient, or if the sender of this message is not the intended recipient, you are hereby notified that any disclosure, or distribution of this document is unauthorized. If you have received this message in error, please notify us by telephone immediately and we will attempt to retrieve the original document for you and we will not be responsible for any further use of the information.

PAGE 4/17 * RCVD AT 2/22/2006 12:32:25 AM [Eastern Standard Time] * SVR:USPTO-EFAXRF-3/15 * DNIS:2736500 * CSID:512 306 8374 * DURATION (mm-ss):04-50

Serial No.: 10/675,432
Response to Office Action

Docket No. 1001.29
Customer No. 53953

In re application of:
Allen Bruce Goodrich

Serial No.: 10/675,432

Group Art Unit: 2186

Filed: September 30, 2003

Examiner: Dare, Ryan A.

For: METHOD AND SYSTEM FOR
REDUCING POWER CONSUMPTION
IN A CACHE MEMORY

Confirmation No.: 6197

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed are the following regarding the above-identified patent application:

1. Transmittal letter (in duplicate);
2. Petition for Extension of Time (in duplicate);
3. Response to Office Action.

☐ Small entity status of this application has previously been established.

☐ No additional fee is required.

The fee has been calculated as shown below:

(Col. 1)		(Col. 2)		(Col. 3)	SMALL ENTITY		OTHER THAN A SMALL ENTITY	
CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE FEE	ADDIT.	OR RATE FEE	ADDIT.
TOTAL	20	minus	20	= 0	x 25	\$_____	OR x 50	\$_____
INDEP	2	minus	3	= 0	x 100	\$_____	OR x 200	\$_____
FIRST PRESENTATION OF MULTIPLE DEP. CLAIM					+ 180	\$_____	OR + 360	\$_____
					TOTAL	\$_____	OR TOTAL	\$_____

Serial No.: 10/675,432
Response to Office Action

Docket No. 1001.29
Customer No. 53953


- ☐ Please charge Deposit Account No. 50-3524 in the amount of \$ ____.
- ☐ A check in the amount of \$ ____ is attached.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-3524.
- ☒ Any additional filing fees under 37 C.F.R. § 1.16 for the presentation of extra claims.
- ☒ Any patent application processing fees under 37 C.F.R. § 1.17.
- ☒ A copy of this sheet is enclosed.

Respectfully submitted,



Michael A. Davis, Jr.
Registration No. 35,488

Date: February 21, 2006
Davis Law Group, P.C.
9020 N. Capital of Texas Hwy.
Bldg. 1, Suite 375
Austin, Texas 78759
Telephone 512-306-8324
Facsimile 512-306-8374
Docket Number: 1001.29

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below:	
Fax Number:	<u>512-273-8300</u>
Michael A. Davis, Jr.	
TYPE OR PRINT NAME	
	
SIGNATURE	
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DATE OF TRANSMISSION	

PTO/SB/22 (08-03)

Approved for use through 7/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PETITION FOR EXTENSION OF TIME UNDER 37 CFR 1.136(a)		Docket Number (Optional) 1001.28
In re Application of Allen Bruce Goodrich		
Application Number 10/675,432		Filed September 30, 2003
METHOD AND SYSTEM FOR REDUCING POWER CONSUMPTION For IN A CACHE MEMORY		
Art Unit 2188		Examiner Dare, Ryan A.

This is a request under the provisions of 37 CFR 1.136(a) to extend the period for filing a reply in the above identified application.

The requested extension and appropriate non-small-entity fee are as follows (check time period desired):

☒ One month (37 CFR 1.17(a)(1)) \$ 120.00

☐ Two months (37 CFR 1.17(a)(2)) \$ _____

☐ Three months (37 CFR 1.17(a)(3)) \$ _____

☐ Four months (37 CFR 1.17(a)(4)) \$ _____

☐ Five months (37 CFR 1.17(a)(5)) \$ _____

☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown above is reduced by one-half, and the resulting fee is: \$ _____.

☐ A check in the amount of the fee is enclosed.

☐ Payment by credit card. Form PTO-2038 is attached.

☐ The Director has already been authorized to charge fees in this application to a Deposit Account.

☒ The Director is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 50-3524.

I have enclosed a duplicate copy of this sheet.

I am the ☐ applicant/inventor.

☐ assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed (Form PTO/SB/96).

☒ attorney or agent of record. Registration Number 35,488

☐ attorney or agent under 37 CFR 1.34(a).
Registration number if acting under 37 CFR 1.34(a) _____

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

February 21, 2008 _____
Date

512-306-8324 _____
Telephone Number

Michael A. Davis, Jr. _____
Typed or printed name

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☒ Total of one forms are submitted.

This collection of information is required by 37 CFR 1.136(e). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 6 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Serial No.: 10/675,432
Response to Office Action

Docket No. 1001.29
Customer No. 53953

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Allen Bruce Goodrich

Serial No.: 10/675,432

Filed: September 30, 2003

For: METHOD AND SYSTEM FOR
REDUCING POWER CONSUMPTION
IN A CACHE MEMORY

§
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Group Art Unit: 2186

Examiner: Dare, Ryan A.

Confirmation No.: 6197

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Please reconsider this application in view of the following remarks.

Amendments to the Specification begin on page 2 of this Response to Office
Action.

Amendments to the Claims are reflected in the listing of claims, which begins on
page 3 of this Response to Office Action.

Remarks begin on page 8 of this Response to Office Action.

Serial No.: 10/675,432
Response to Office Action

Docket No. 1001.29
Customer No. 53953

Amendments to the Specification

Please replace the paragraph beginning at page 16, line 3, with the following rewritten paragraph.

--In response to the start of a next clock cycle $k+2-k+1$, the latch 148 receives and stores such information from the words W_{xy} . Accordingly, via the control logic 100 and bus 116, the multiplexer 112 outputs (to the latch 148) either: (a) the words W_{x0} through W_{x07} if the match is with Address Tag_{x0} , (b) the words W_{x10} through W_{x17} if the match is with Address Tag_{x1} , (c) the words W_{x20} through W_{x27} if the match is with Address Tag_{x2} , or (d) the words W_{x30} through W_{x37} if the match is with Address Tag_{x3} .--

Serial No.: 10/675,432
Response to Office Action

Docket No. 1001.29
Customer No. 53953

Amendments to the Claims

The following listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently amended) A method of reducing power consumption in an N -way-a multi-way set-associative cache memory having Y sets, wherein N is a first integer, and wherein Y is a second integer, the method comprising:

during a first clock cycle k , in response to an address, identifying an associated set in the cache memory, comparing the address to respective tag portions of N blocks in the associated set, and outputting a signal in response thereto, wherein k is an integer; and

during a second clock cycle $k+1$, in response to the signal indicating that a match between one of the N blocks in the associated set is a match with and the address, reading a non-tag portion of the matching block in the associated set, while non-tag portions of $N-1$ non-matching blocks-a non-matching block in the associated set are is disabled, and while non-tag portions of $Y-1$ non-associated sets are disabled.

2. (Previously presented) The method of Claim 1, wherein the reading comprises:

enabling the non-tag portion of the matching block in the associated set.

3. (Previously presented) The method of Claim 2, wherein the enabling comprises:

applying power to the non-tag portion of the matching block in the associated set.

4. (Currently amended) The method of Claim 1, and comprising:

removing power from at least one of the non-tag portions of: the $N-1$ non-matching blocks-block in the associated set; and the $Y-1$ non-associated sets.

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Response to Office Action

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5. (Currently amended) The method of Claim 4, wherein the removing power comprises:
removing power from the at least one of the non-tag portions ~~the non-matching block~~
~~in the associated set~~, so that ~~it the non-matching block in the associated set~~ is disabled from
outputting information, and so that ~~it the non-matching block in the associated set~~ continues
to store the information.
6. (Previously presented) The method of Claim 1, wherein the cache memory is
a program cache.
7. (Previously presented) The method of Claim 1, wherein the cache memory is
a data cache.
8. (Currently amended) The method of Claim 1, wherein the comparing the
address comprises:
comparing a portion of the address to respective tag portions of the N blocks in the
associated set.
9. (Currently amended) The method of Claim 1, wherein the reading the non-tag
portion comprises:
reading the non-tag portion of the matching block in the associated set, while the non-
tag portions of the N-1 non-matching blocks ~~block~~ in the associated set ~~are~~ is at least partly
disabled, and while the non-tag portions of the Y-1 non-associated sets are at least partly
disabled.

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10. (Currently amended) The method of Claim 1, and comprising wherein reading the non-tag portion comprises:

during the second clock cycle $k+1$, in response to a second address, identifying a second associated set in the cache memory, comparing the second address to respective tag portions of N blocks in the second associated set, and outputting a second signal in response thereto; and

during a third clock cycle $k+2$, in response to the second signal indicating that one of the N blocks in the second associated set is a match with the second address, reading a non-tag portion of the matching block in the second associated set, while non-tag portions of $N-1$ non-matching blocks in the second associated set are disabled, and while non-tag portions of $Y-1$ non-associated sets are disabled reading the non-tag portion of the matching block in the associated set, while at least first and second non-matching blocks in the associated set are disabled.

11. (Currently amended) A system for reducing power consumption in an N -way a multi-way set-associative cache memory having Y sets, wherein N is a first integer, and wherein Y is a second integer, the system comprising:

first circuitry for: during a first clock cycle k , in response to an address, identifying an associated set in the cache memory, comparing the address to respective tag portions of N blocks in the associated set, and outputting a signal in response thereto, wherein k is an integer; and

second circuitry for: during a second clock cycle $k+1$, in response to the signal indicating ~~that a match between one of the N blocks in the associated set is a match with~~ and the address, reading a non-tag portion of the matching block in the associated set, while ~~non-tag portions of $N-1$ non-matching blocks~~ a non-matching block in the associated set ~~are~~ is disabled, and while non-tag portions of $Y-1$ non-associated sets are disabled.

12. (Previously presented) The system of Claim 11, wherein the second circuitry is for enabling the non-tag portion of the matching block in the associated set.

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Customer No. 53953

13. (Previously presented) The system of Claim 12, wherein the second circuitry is for applying power to the non-tag portion of the matching block in the associated set.

14. (Currently amended) The system of Claim 11, wherein the second circuitry is for removing power from at least one of the non-tag portions of: the N-1 non-matching blocks block in the associated set; and the Y-1 non-associated sets.

15. (Currently amended) The system of Claim 14, wherein the second circuitry is for removing power from the at least one of the non-tag portions ~~the non-matching block in the associated set~~, so that it ~~the non-matching block in the associated set~~ is disabled from outputting information, and so that it ~~the non-matching block in the associated set~~ continues to store the information.

16. (Previously presented) The system of Claim 11, wherein the cache memory is a program cache.

17. (Previously presented) The system of Claim 11, wherein the cache memory is a data cache.

18. (Currently amended) The system of Claim 11, wherein the first circuitry is for comparing a portion of the address to respective tag portions of the N blocks in the associated set.

19. (Currently amended) The system of Claim 11, wherein the second circuitry is for reading the non-tag portion of the matching block in the associated set, while the non-tag portions of the N-1 non-matching blocks block in the associated set are ~~is~~ at least partly disabled, and while the non-tag portions of the Y-1 non-associated sets are at least partly disabled.

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Response to Office Action

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Customer No. 53953

20. (Currently amended) The system of Claim 11, wherein;

the first circuitry is for: during the second clock cycle $k+1$, in response to a second address, identifying a second associated set in the cache memory, comparing the second address to respective tag portions of N blocks in the second associated set, and outputting a second signal in response thereto; and

the second circuitry is for: during a third clock cycle $k+2$, in response to the second signal indicating that one of the N blocks in the second associated set is a match with the second address, reading a non-tag portion of the matching block in the second associated set, while non-tag portions of $N-1$ non-matching blocks in the second associated set are disabled, and while non-tag portions of $Y-1$ non-associated sets are disabled reading the non-tag portion of the matching block in the associated set, while at least first and second non-matching blocks in the associated set are disabled.

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Response to Office Action

Docket No. 1001.29
Customer No. 53953

REMARKS

Applicant respectfully requests reconsideration of this application in view of the following remarks. Claims 1, 4, 5, 8, 9, 10, 11, 14, 15, 18, 19 and 20 have been amended. Claims 1-20 are pending. Antecedent basis for the amendments is located throughout Applicant's specification and the original claims, as for example in connection with the discussion of Figs. 3 and 4 at page 11, line 22 through page 16, line 22. Accordingly, no new matter has been entered.

Also, the specification has been amended to correct a typographical error. At page 15, line 23 through page 16, line 2, the specification states, "During the clock cycle $k+1$...the multiplexer 112 outputs information from such words W_{xy} of the address's associated Set x to the latch 148 via the control logic 100 and bus 116." In the immediately following sentence, at page 16, lines 3-4, the specification states, "In response to the start of a next clock cycle $k+1$, the latch 148 receives and stores such information from the words W_{xy} ." Clearly, therefore, at page 16, line 3, "clock cycle $k+1$ " is a typographical error. Accordingly, no new matter has been entered.

Rejection of the claims

The Office Action rejected claims 1 and 11 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent Application Publication No. 2004/0210728 ("Flautner").

As amended, claim 1 recites:

1. A method of reducing power consumption in an N -way set-associative cache memory having Y sets, wherein N is a first integer, and wherein Y is a second integer, the method comprising:

during a first clock cycle k , in response to an address, identifying an associated set in the cache memory, comparing the address to respective tag portions of N blocks in the associated set, and outputting a signal in response thereto, wherein k is an integer; and

during a second clock cycle $k+1$, in response to the signal indicating that one of the N blocks in the associated set is a match with the address, reading a non-tag portion of the matching block in the associated set, while non-tag portions of $N-1$ non-matching blocks in the associated set are disabled, and while non-tag portions of $Y-1$ non-associated sets are disabled.

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Response to Office Action

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Customer No. 53953

As amended, claim 11 recites:

11. A system for reducing power consumption in an N -way set-associative cache memory having Y sets, wherein N is a first integer, and wherein Y is a second integer, the system comprising:

first circuitry for: during a first clock cycle k , in response to an address, identifying an associated set in the cache memory, comparing the address to respective tag portions of N blocks in the associated set, and outputting a signal in response thereto, wherein k is an integer; and

second circuitry for: during a second clock cycle $k+1$, in response to the signal indicating that one of the N blocks in the associated set is a match with the address, reading a non-tag portion of the matching block in the associated set, while non-tag portions of $N-1$ non-matching blocks in the associated set are disabled, and while non-tag portions of $Y-1$ non-associated sets are disabled.

In MPEP § 2131, the PTO provides that:

"[t]o anticipate a claim, the reference must teach every element of the claim...."

Therefore, to sustain a rejection of amended claim 1, Flautner must contain all of the above-recited elements in amended claim 1. However, Flautner fails to teach the combination of elements in amended claim 1. In fact, the Office Action's cited portion of Flautner actually teaches away from such a combination.

Accordingly, Flautner fails to support a rejection of amended claim 1 under 35 U.S.C. § 102(e). In relation to amended claim 11, Flautner is likewise defective in supporting a rejection under 35 U.S.C. § 102(e).

Likewise, in relation to amended claim 1, Flautner is defective in supporting a prima facie case of obviousness. As between Flautner and Applicant's specification, only Applicant's specification teaches the combination of elements in amended claim 1. In fact, the Office Action's cited portion of Flautner actually teaches away from such a combination.

Accordingly, in relation to amended claim 1, the PTO's burden of factually supporting a prima facie case of obviousness has not been met. In relation to amended claim 11, Flautner is likewise defective in supporting a prima facie case of obviousness.

Thus, a rejection of amended claims 1 and 11 is not supported.

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Conclusion

For these reasons, and for other reasons clearly apparent, Applicant respectfully requests allowance of claims 1 and 11.

Dependent claims 2-10 depend from and further limit claim 1 and therefore are allowable.

Dependent claims 12-20 depend from and further limit claim 11 and therefore are allowable.

An early formal notice of allowance of claims 1-20 is requested.

To the extent that this Response to Office Action results in additional fees, the Commissioner is authorized to charge deposit account no. 50-3524.


Applicant has made an earnest attempt to place this case in condition for allowance. If any unresolved aspect remains, the Examiner is invited to call Applicant's attorney at the telephone number listed below.

Respectfully submitted,



Michael A. Davis, Jr.
Registration No. 35,488

Date: February 21, 2006
Davis Law Group, P.C.
9020 N. Capital of Texas Hwy.
Bldg. 1, Suite 375
Austin, Texas 78759
Telephone 512-306-8324
Facsimile 512-306-8374
Docket Number: 1001.29

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below:	
Fax Number:	<u>512-273-8300</u>
Michael A. Davis, Jr.	
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February 21, 2006	
DATE OF TRANSMISSION	

Fax Transmittal**DATE:** April 24, 2006**Total Pages (including this cover page):** 17

TO: Refund Branch
U.S. Patent and Trademark
P.O. Box 1450
Alexandria, VA 22313-1450

Fax No.: 571-273-6500

FROM: Michael A. Davis, Jr. Davis Law Group, P.C.
Direct Phone #: (512) 306-8324 9020 N. Capital of Texas Hwy.
Direct Fax #: (512) 306-8374 Bldg. I, Suite 375
E-mail: michael.davis@LegalVP.com Austin, Texas USA 78759
If you have any problem with this transmittal, please call: (512) 306-8324

RE: Deposit Account No. 50-3524 (Refund Request - SECOND SUBMITTAL)
Customer No. 53953

Please see the attached correspondence regarding a request for refund.

Confidentiality Note: The information contained in this facsimile message is privileged and confidential and is intended only for the use of the addressee. The term "privileged and confidential" includes, without limitation, attorney-client privileged communications, attorney work product, trade secrets, and any other proprietary information. Nothing in this facsimile is intended by the attorney or the client to constitute a waiver of the confidentiality of this message. If the reader of this message is not the intended recipient, or employee/agent of the intended recipient, you are hereby notified that any duplication, or distribution of this communication is unauthorized. If you have received this message in error, please notify us by telephone immediately so that we can arrange for the return of the original documents to us at no cost to you.

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